



**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

Confirmation Number: 4127

Hiroyuki ABE et al.

Group Art Unit: 2816

Application No.: 10/628,580

Examiner: Anh Quan Tra

Filed: July 29, 2003

Attorney Dkt. Number: 108066-00092

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING CONTROLLABLE  
INTERNAL SUPPLY VOLTAGE

**RESPONSE UNDER 37 C.F.R. § 1.116**

**ATTN: Mail Stop AF**  
Director for the U.S. PTO  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: July 13, 2005

Sir:

This paper is filed in response to the Office Action dated April 13, 2005. In the prosecution of the application identified above, please consider the following.

Amendments to the claims begin on page 2.

Remarks/arguments begin on page 11.

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